

PATENT NUMBER

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<p>O.I.P.E.</p> <p>SCANNED <u>KS</u> <u>LMZ</u> Q.A. <u>HN</u></p>	PATENT DATE
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APPLICATION NO. 09/942907	CONT/PRIOR F	CLASS 438	SUBCLASS 424	ART UNIT 2812 3800	EXAMINER Thomas
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Tsuruno Ikura

**TITLE**

Method for fabricating semiconductor device

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PTO-2040  
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____				<b>ISSUE FEE</b>	
	Amount Due	Date Paid			
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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